

FIG. 1 (PRIOR ART)

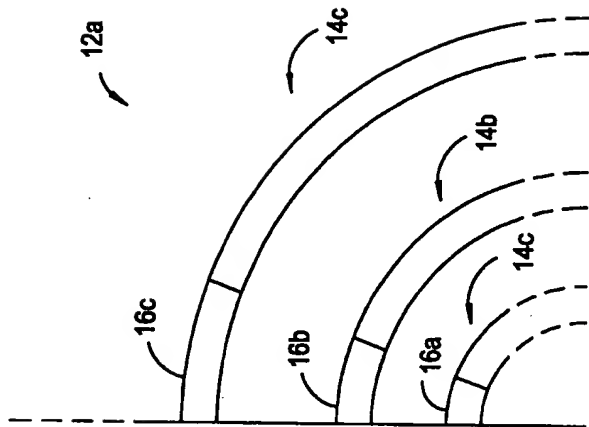


FIG. 2 (PRIOR ART)

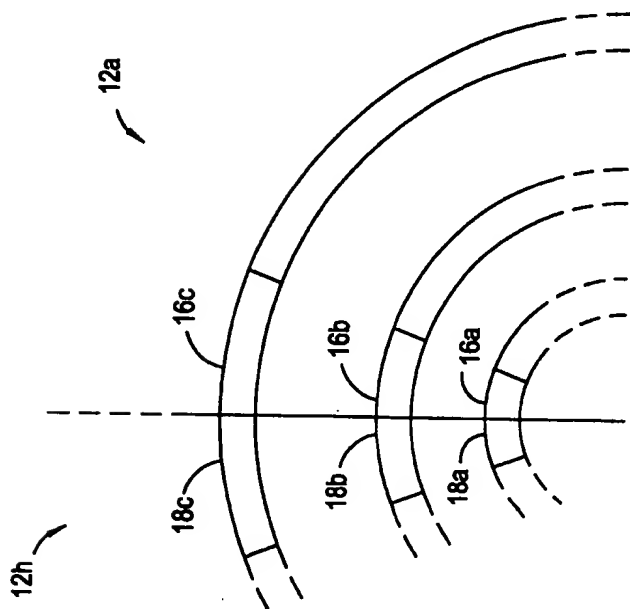


FIG. 3 (PRIOR ART)

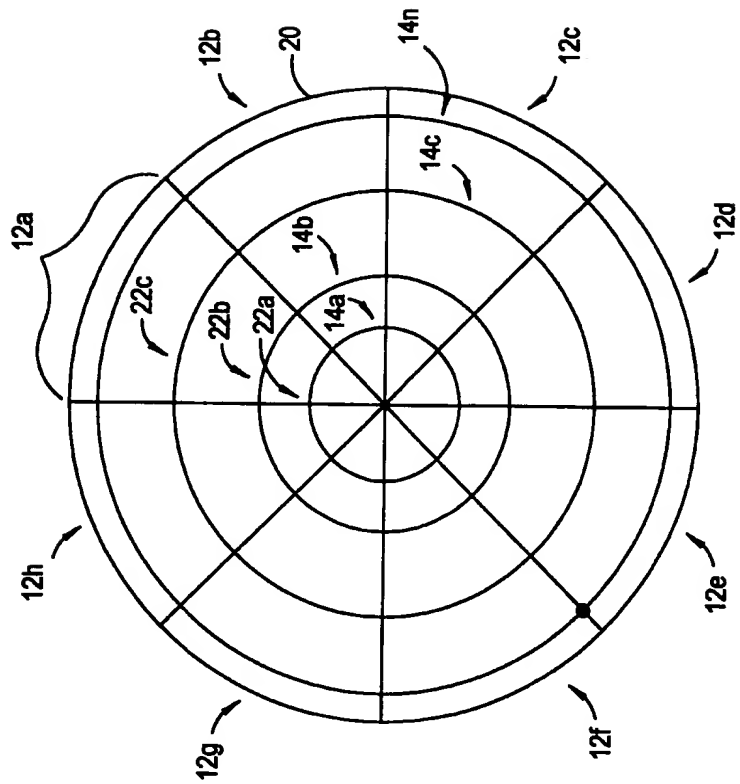
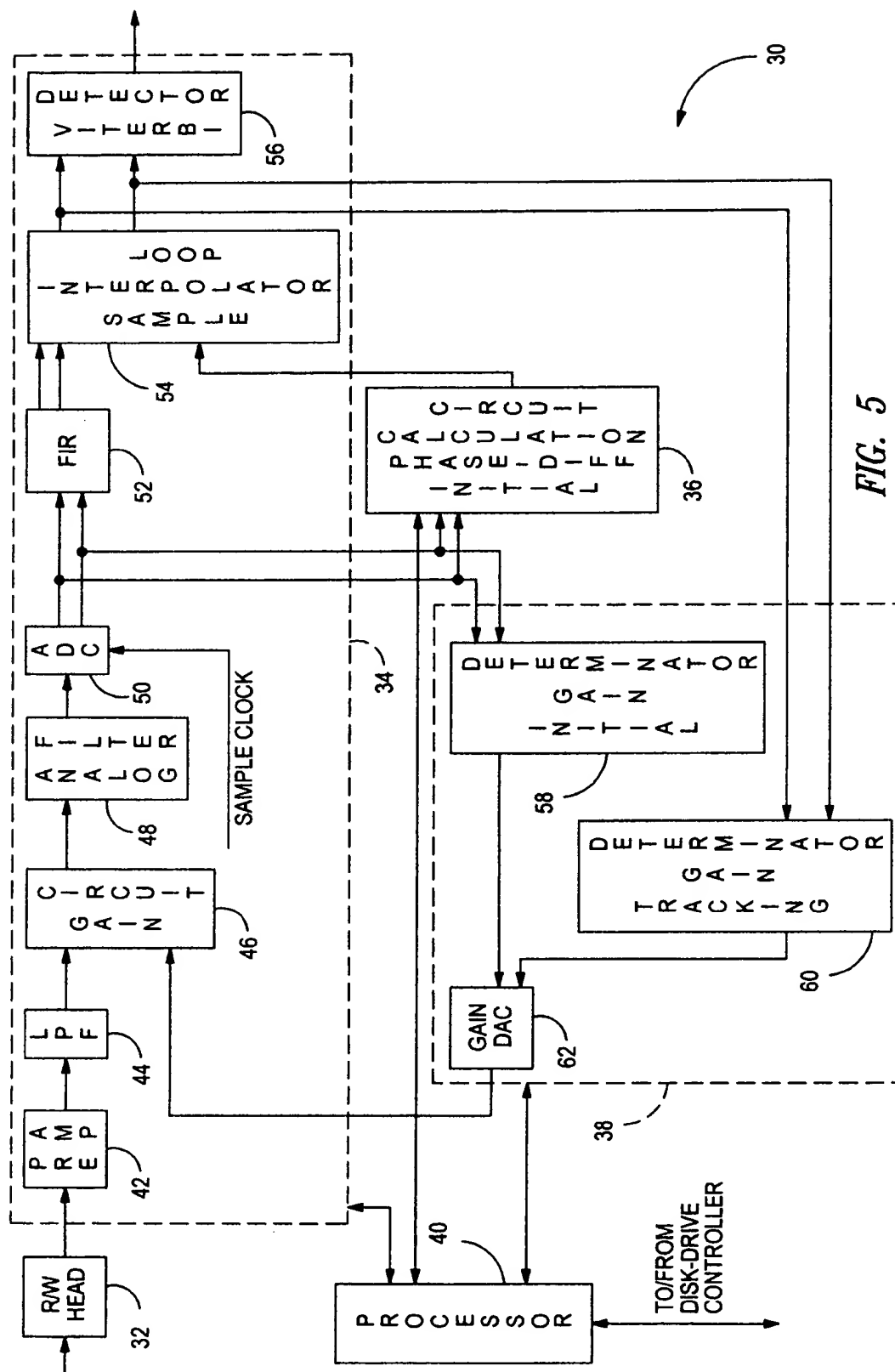


FIG. 4



22a

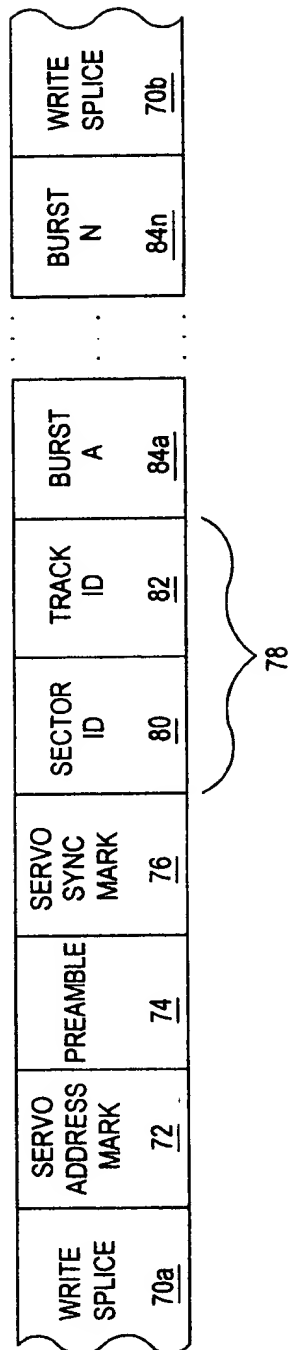


FIG. 6

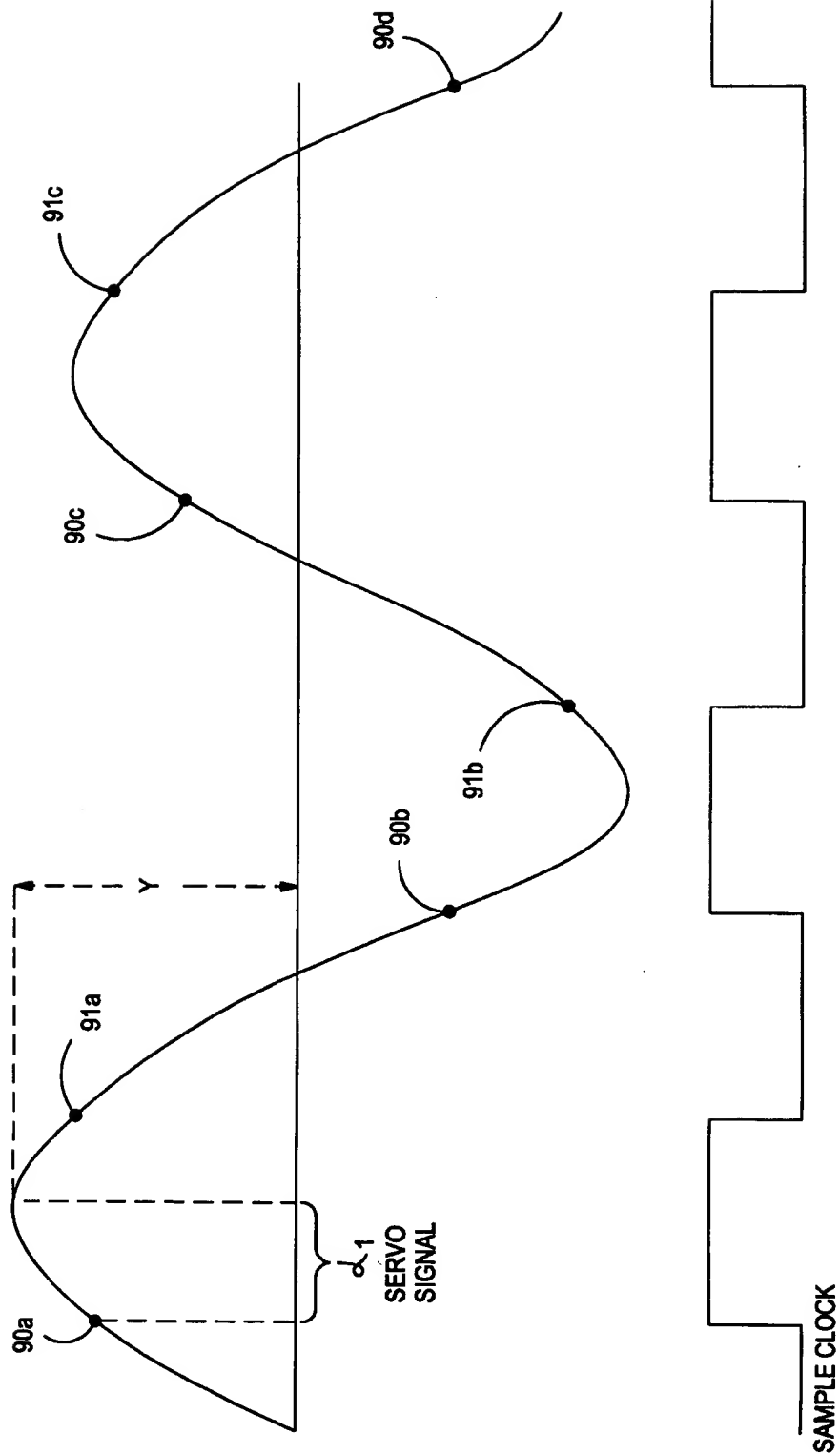


FIG. 7

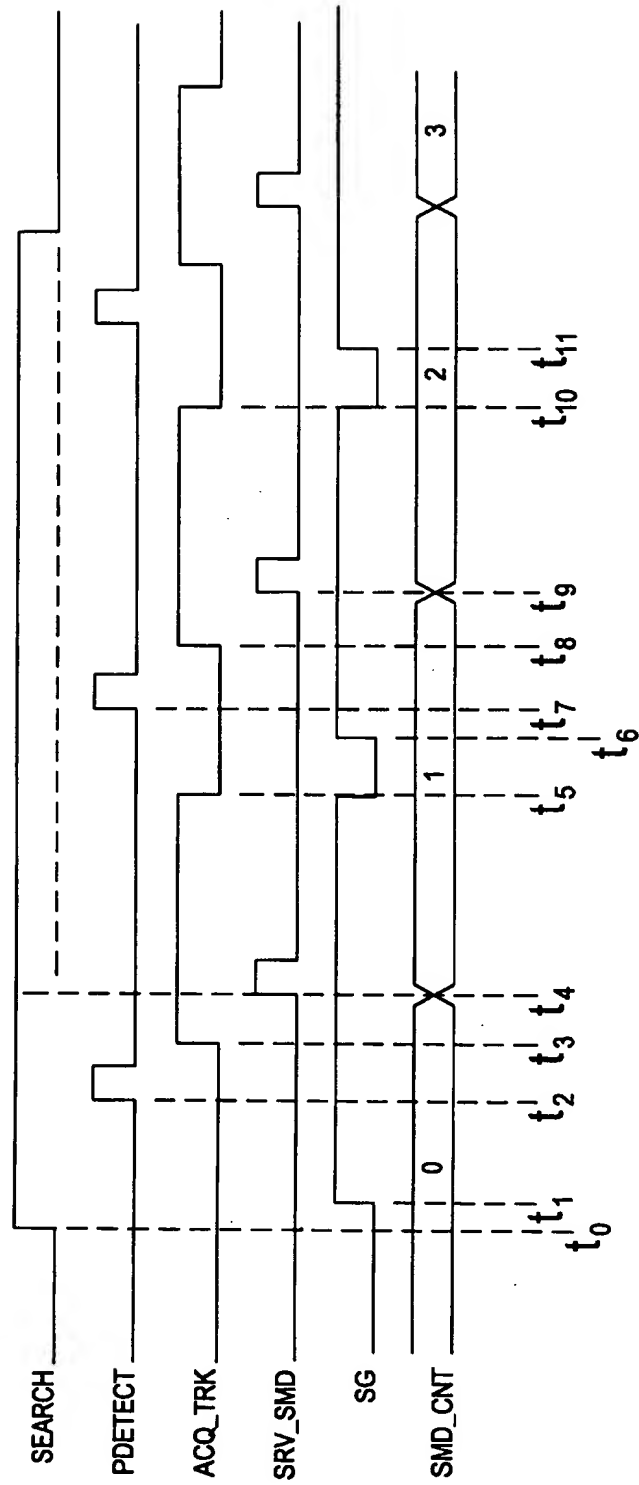


FIG. 8

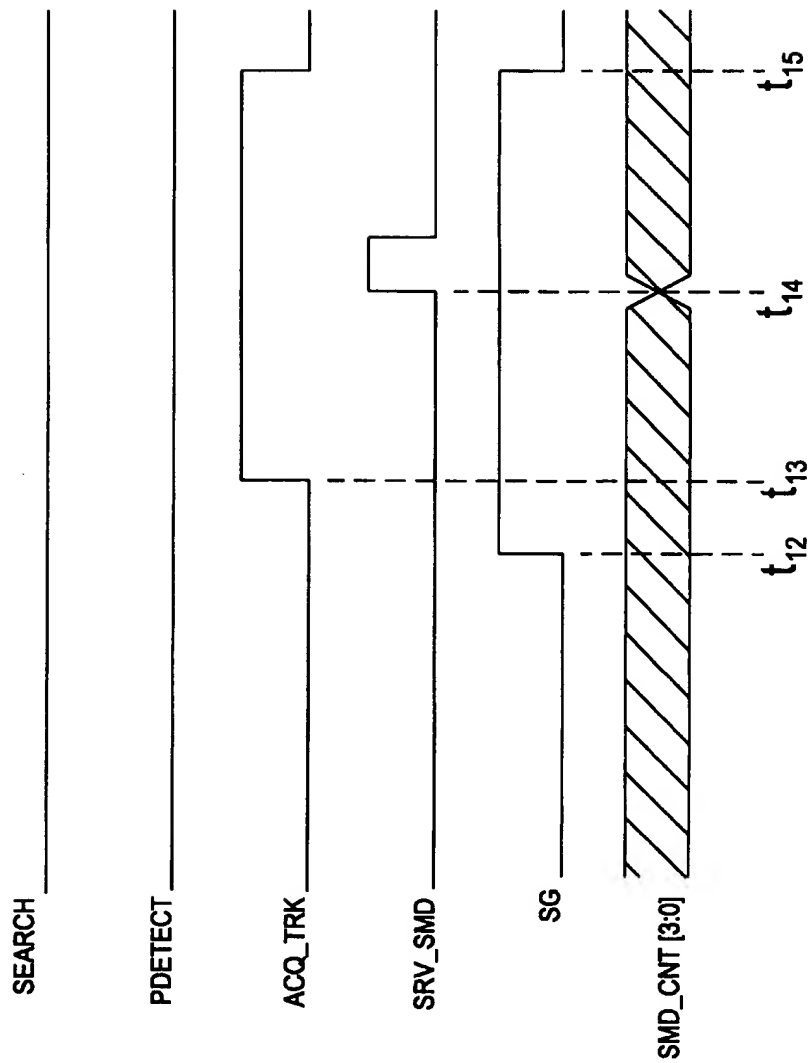


FIG. 9

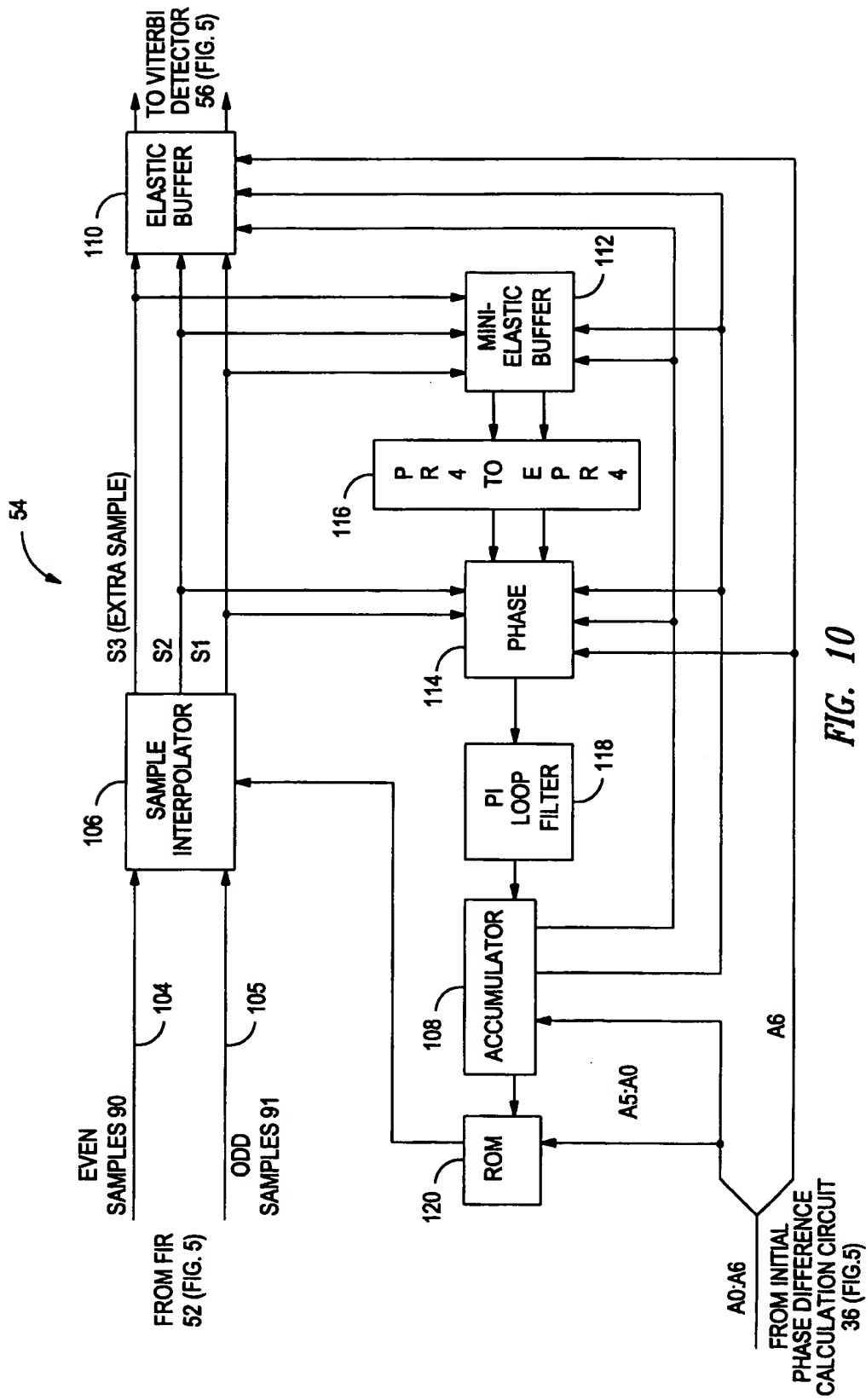


FIG. 10



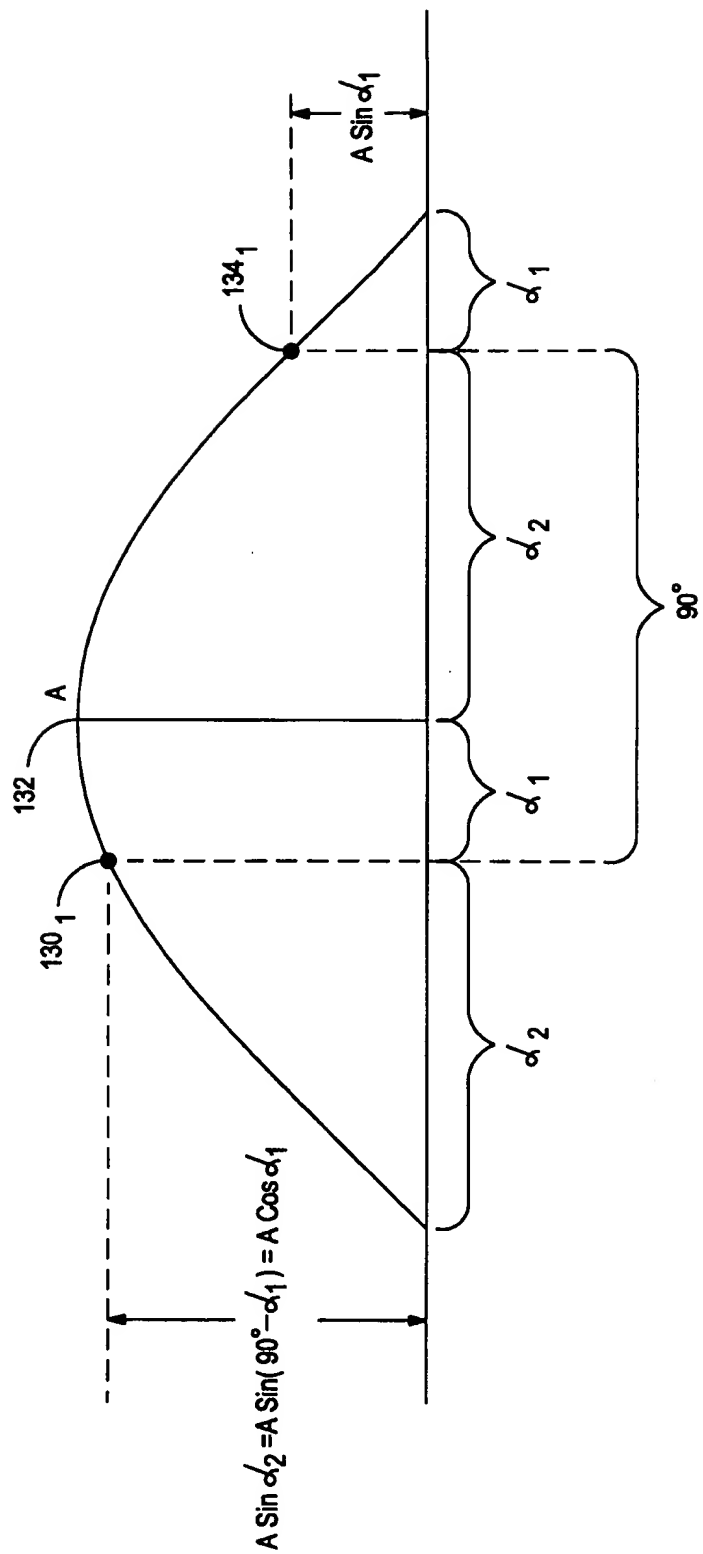


FIG. 11

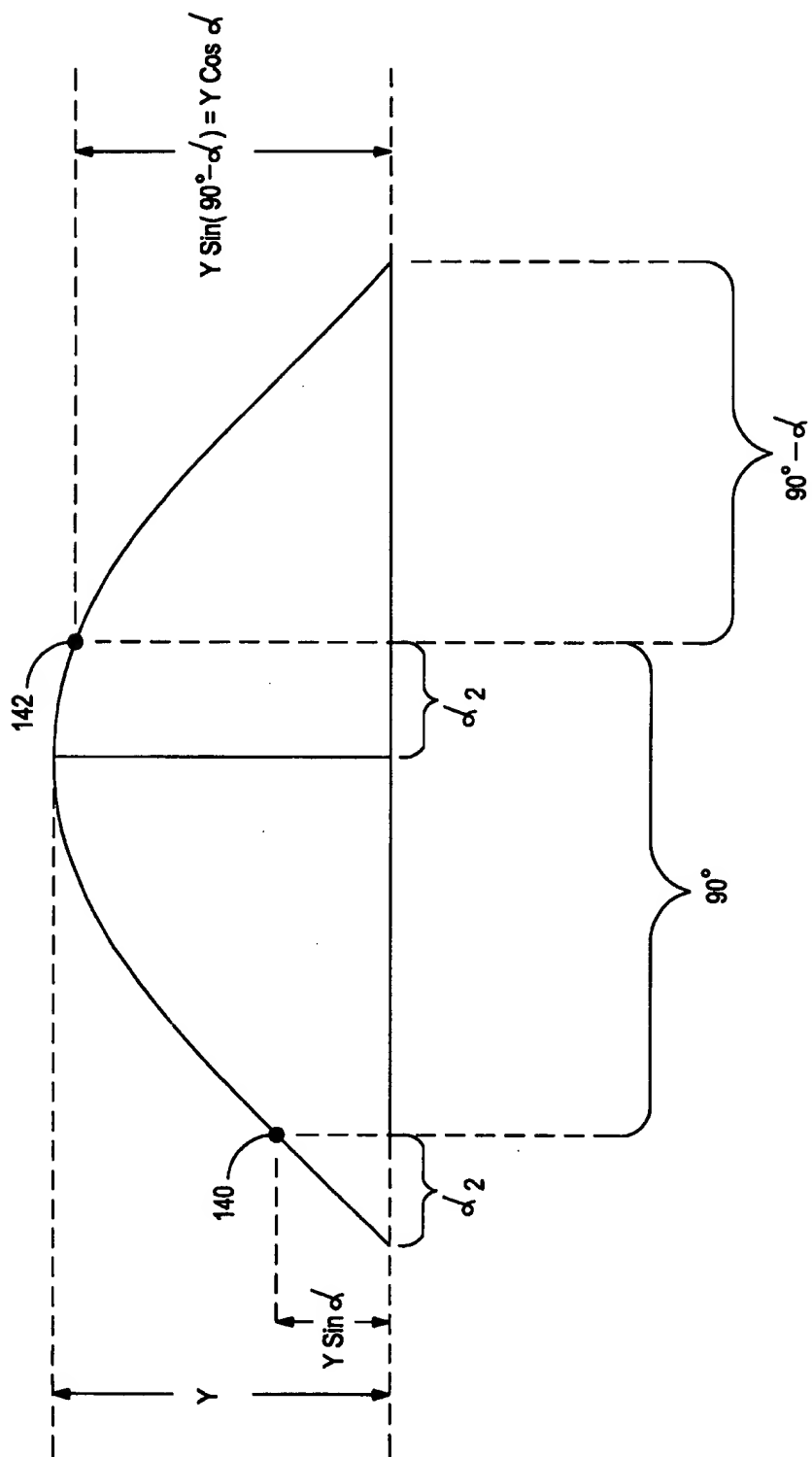


FIG. 12

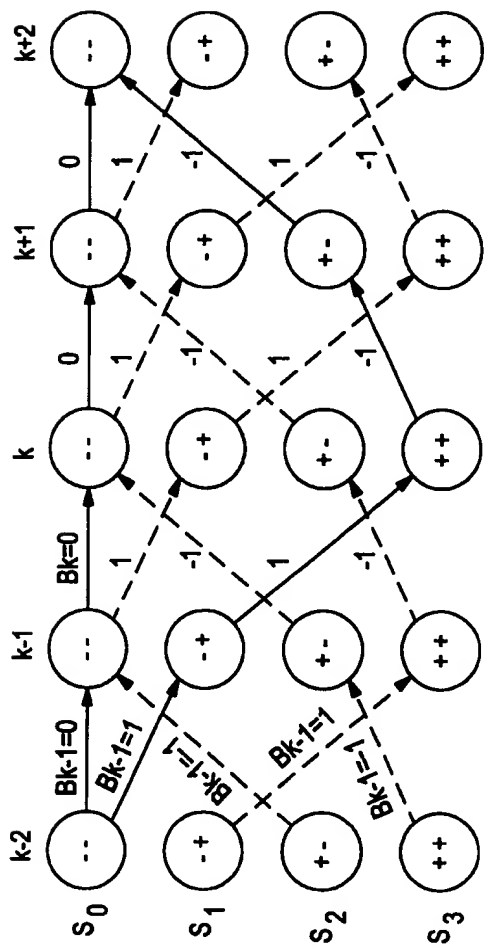


FIG. 13

 PREAMBLE
  SERVO SYNCHRONIZATION MARK
  SECTOR ID

0011001100110000000011000000110000011XXXXXXX

FIG. 14

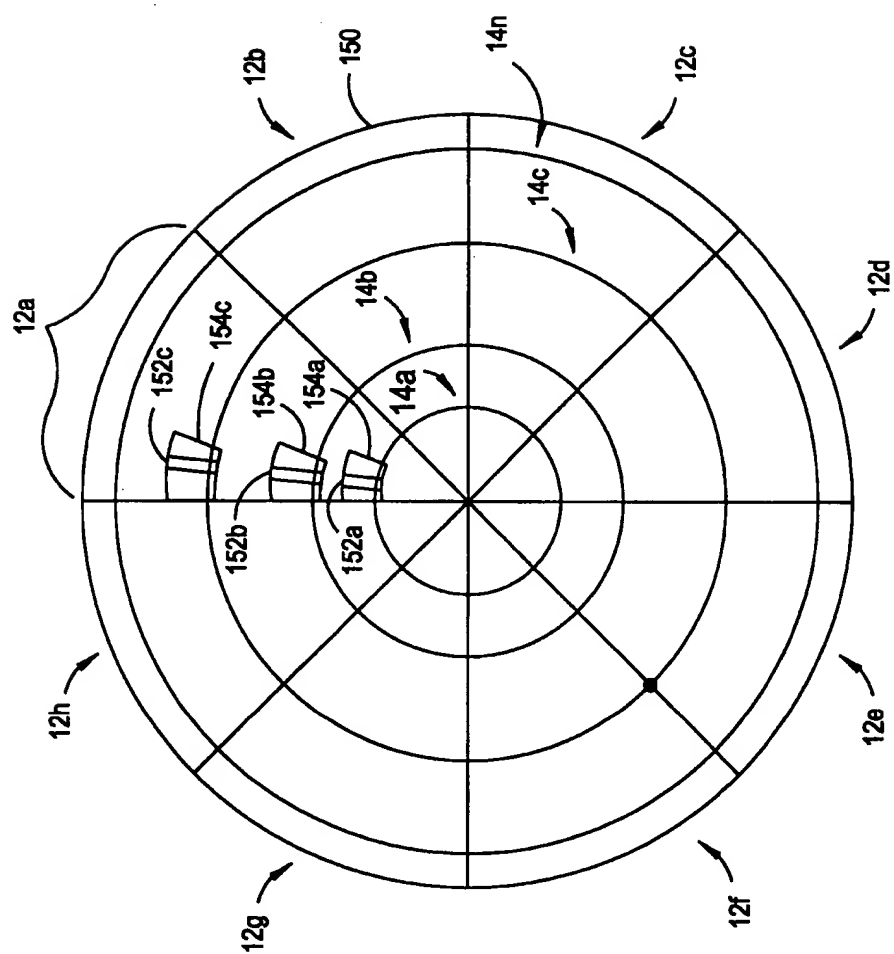


FIG. 15

154a

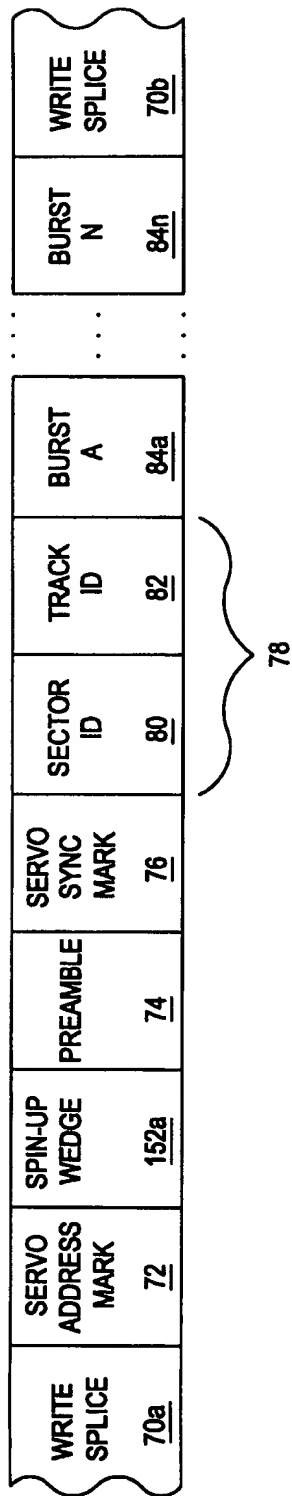


FIG. 16

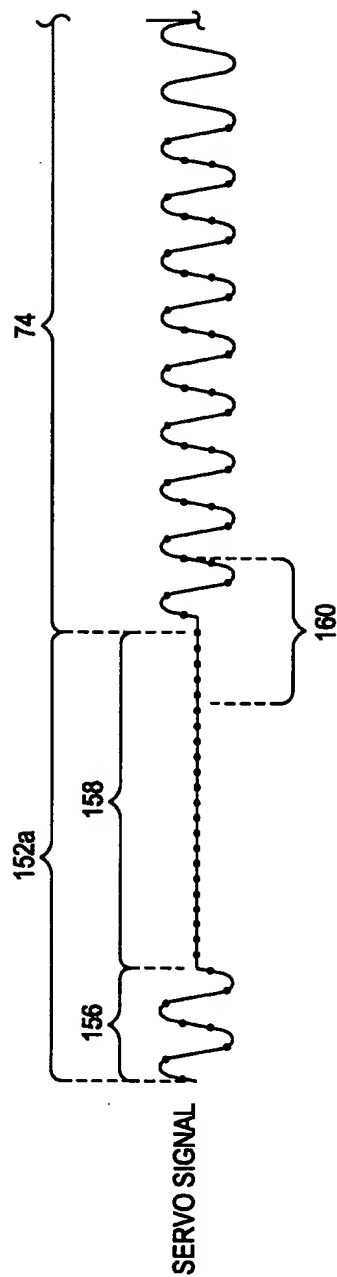


FIG. 17

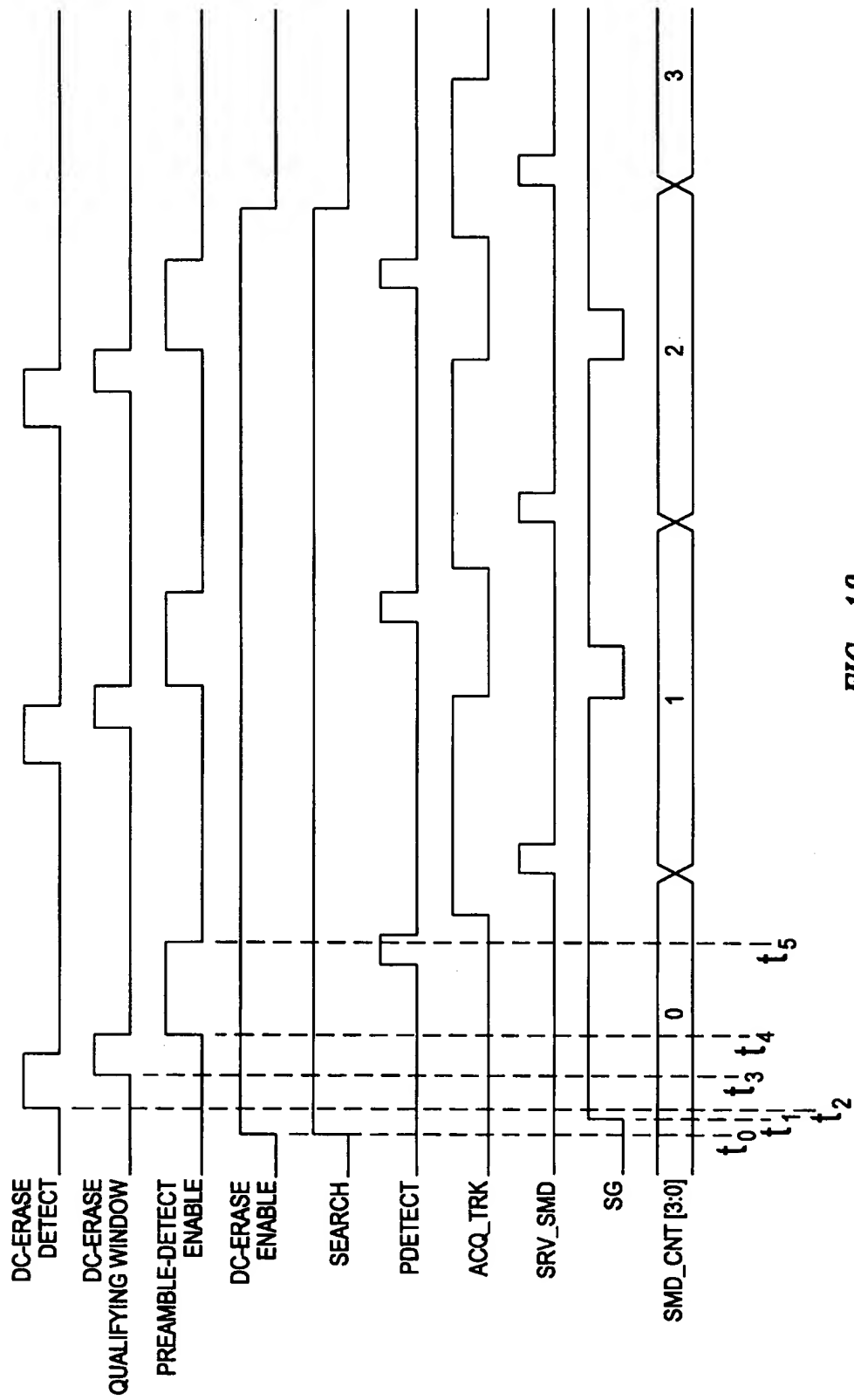


FIG. 18

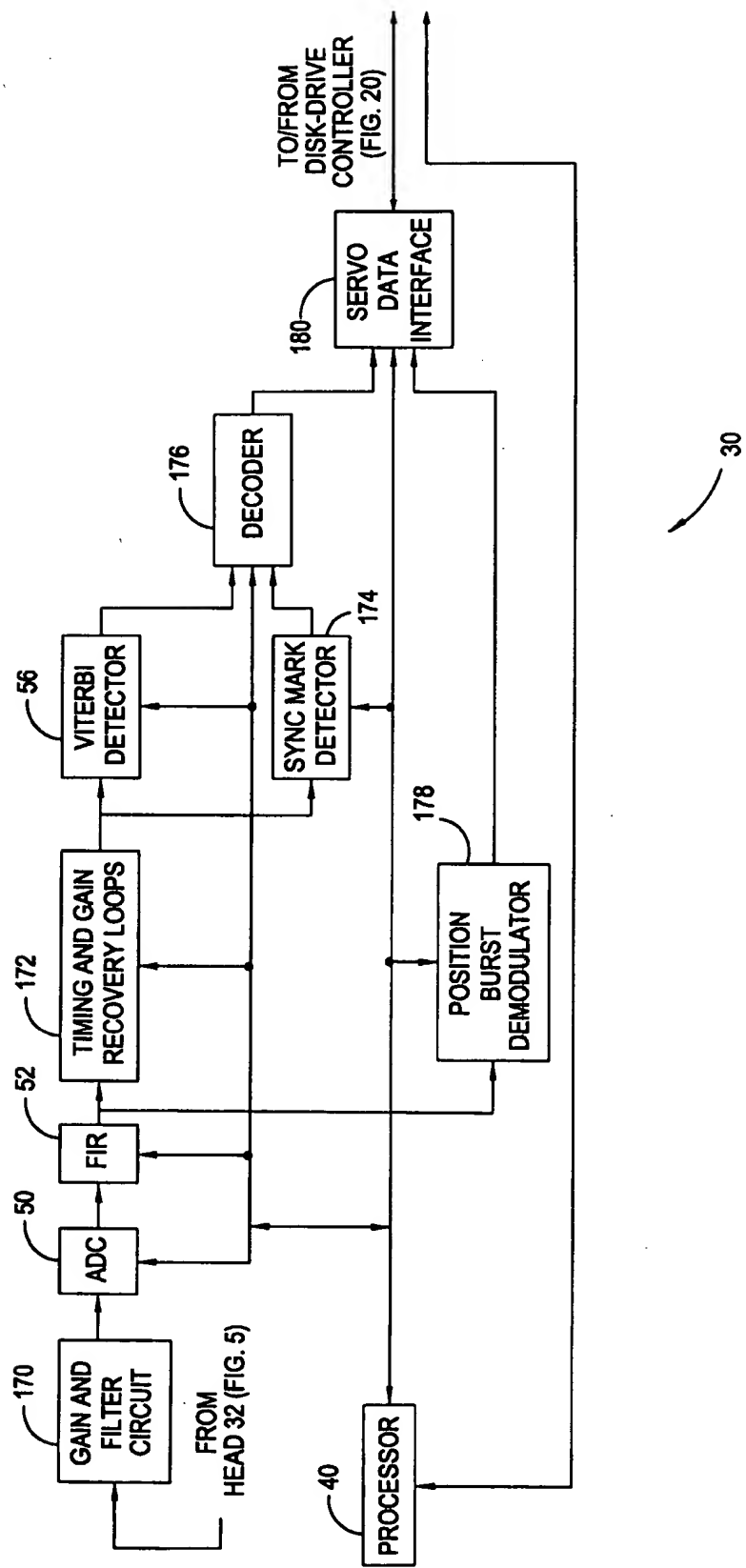


FIG. 19

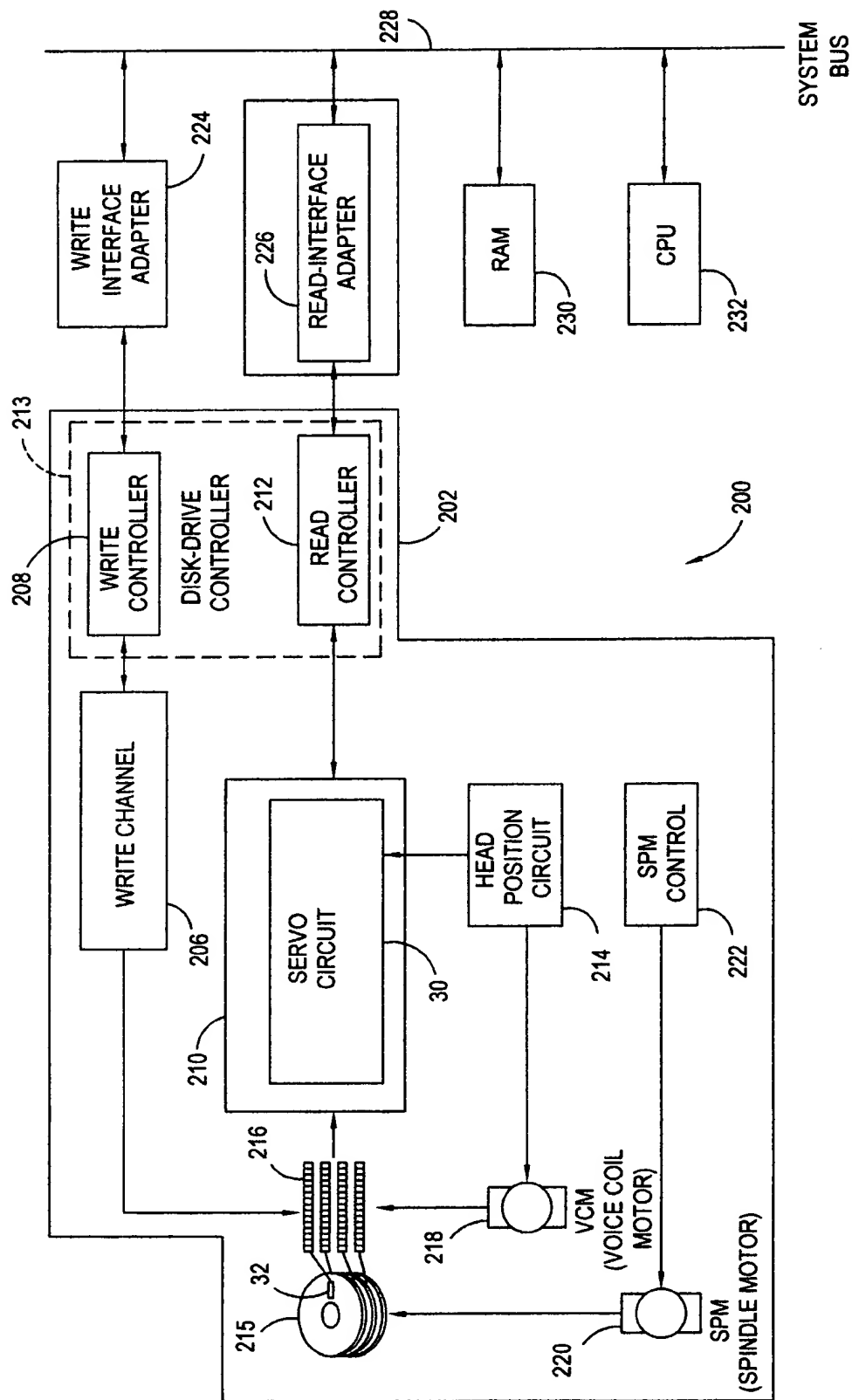


FIG. 20